Serial No. of Attorney Docket No. Form PTO-1449 (modified) Parent Application LIST OF PATENTS AND PUBLICATIONS 2207/793203 10/060,264 FOR APPLICANT(S)' INFORMATION DISCLOSURE STATEMENT **Applicants** Amit A. MERCHANT et al. Filing Date of Group Art Unit of **Parent Application Parent Application**

U. S. PATENT DOCUMENTS

February 1, 2002

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	CLASS SUBCLASS		FILING DATE		
7	5,185,871	February 9,1993	Frey et al. 712 205		December 26, 1989		
70	5,197,132	March 23,1993	Steely, Jr. et al. 395 375 June 29,		June 29, 1990		
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^{**}A copy is not enclosed since the present application was filed after June 30, 2003. (See OG Dated: August 5, 2003)

FOREIGN PATENT DOCUMENTS

						TRANSL	ATION
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	YES	NO

OTHER DOCUMENTS

EXAMINER	·				
INITIAL	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.				
	Wallace et al., Threaded Multiple Path Execution, June 1998, pp.1-12.				
	Roos et al., Execution Replay of Parallel Programs, 1993, pp. 1-6.				
	Marcuello et al., Data Speculative Multithreaded Architecture, 1998, pp. 321-324.				
	Gulati et al., Performance Study of a Multithreaded Superscalar Microprocessor, 1996, pp. 291-301.				
	Akkary et al., A Dynamic Multithreading Processor, 1998, pp. 226-236.				
8/	Tsai et al., Performance Study of a Concurrent Multithreaded Processor, 1998, pp. 24-35.				
	Lolkkanen et al., A Fine-Grain Multithreading Superscalar Architecture, 1996, pp. 163-168.				

EXAMINER

DATE CONSIDERED 04

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EXAMINER: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.